

FIG. 1 (PRIOR ART)

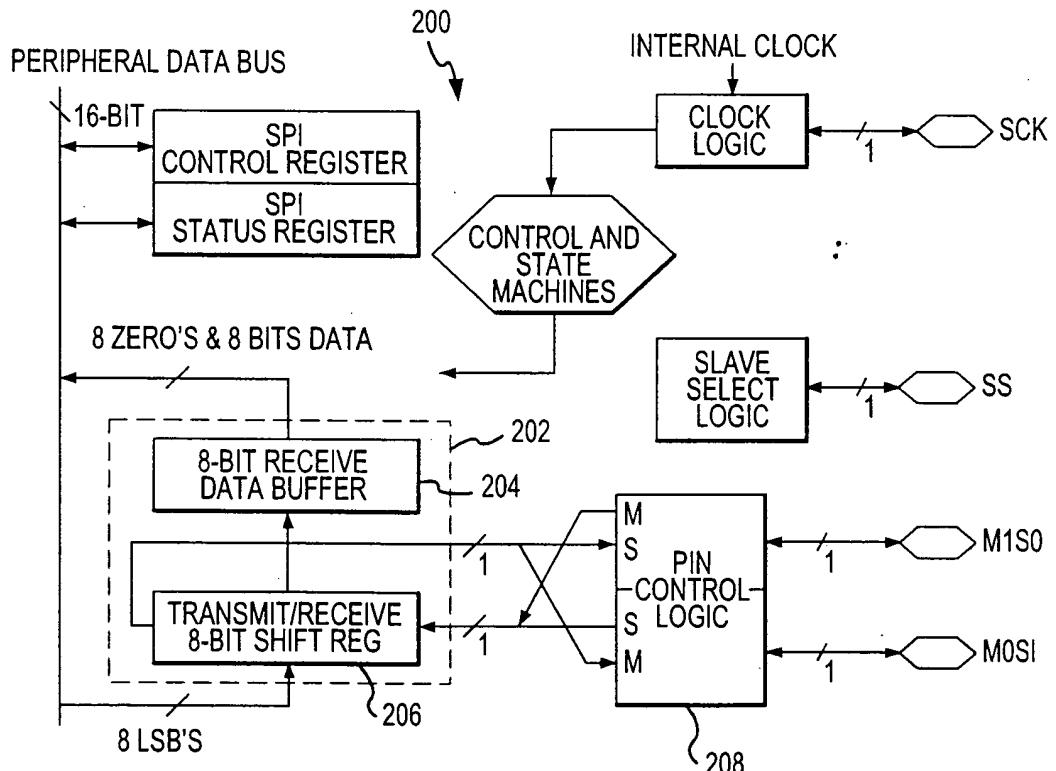


FIG.2 (PRIOR ART)

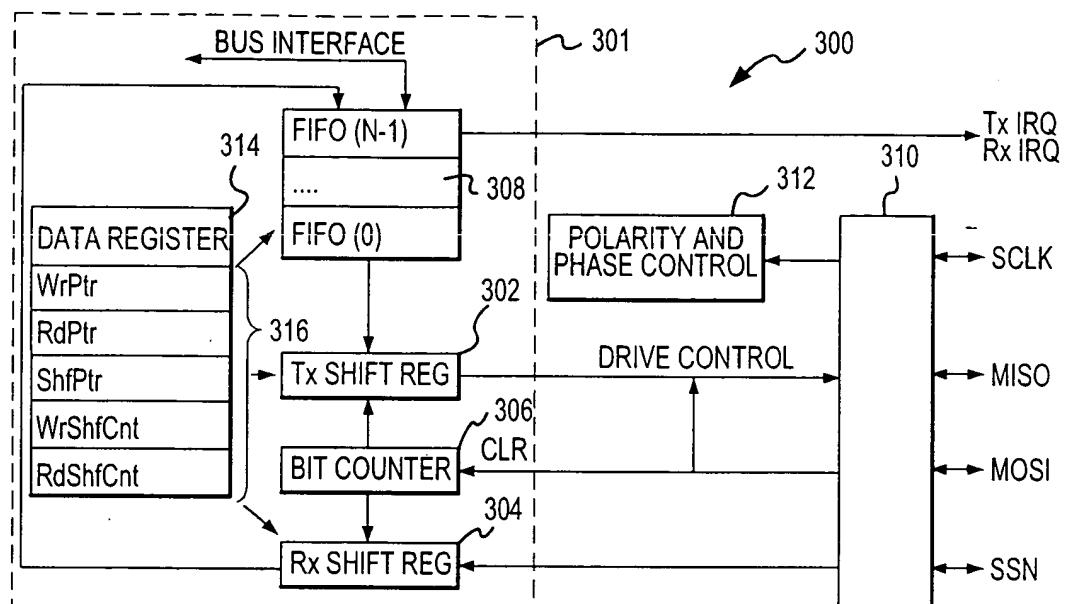


FIG.3

SPI/GPIO CONTROL

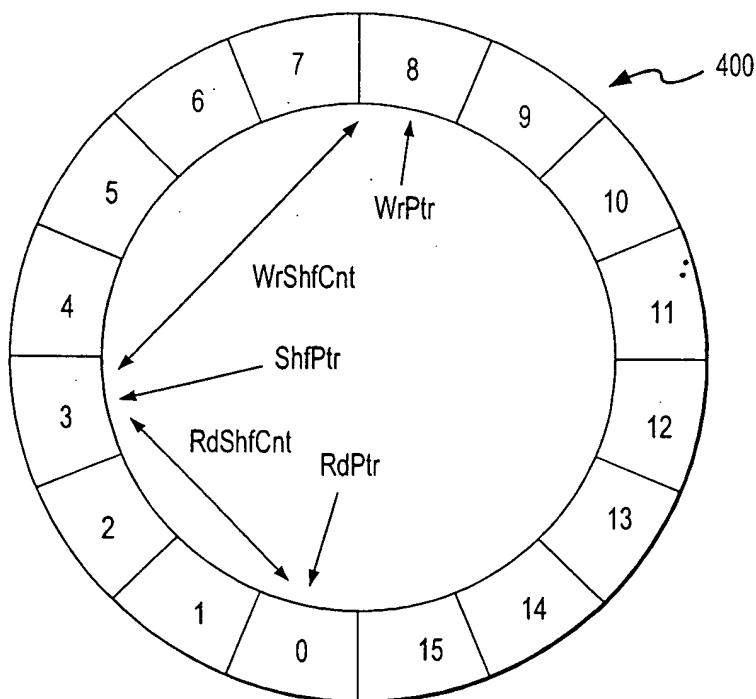


FIG.4

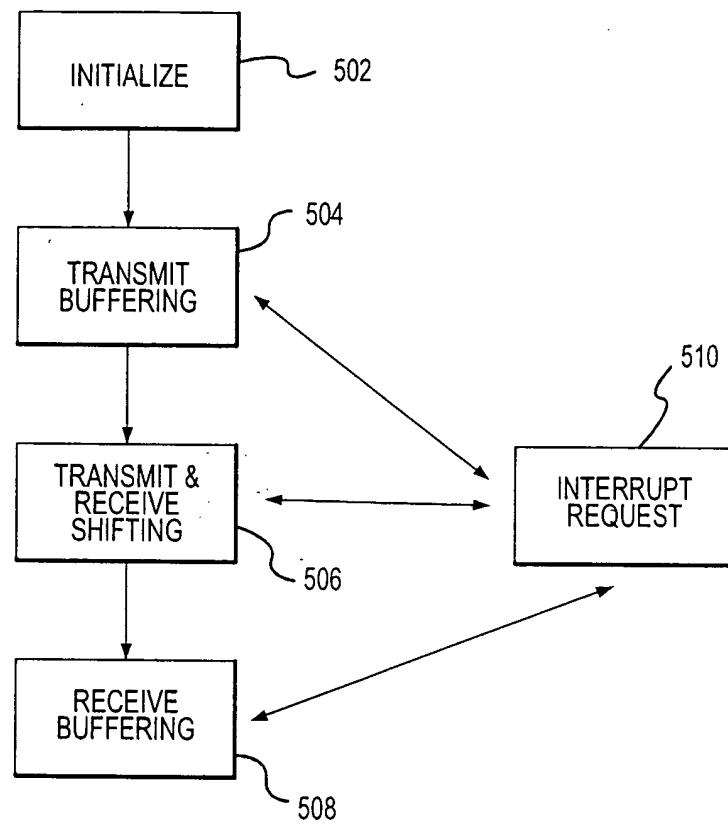


FIG.5

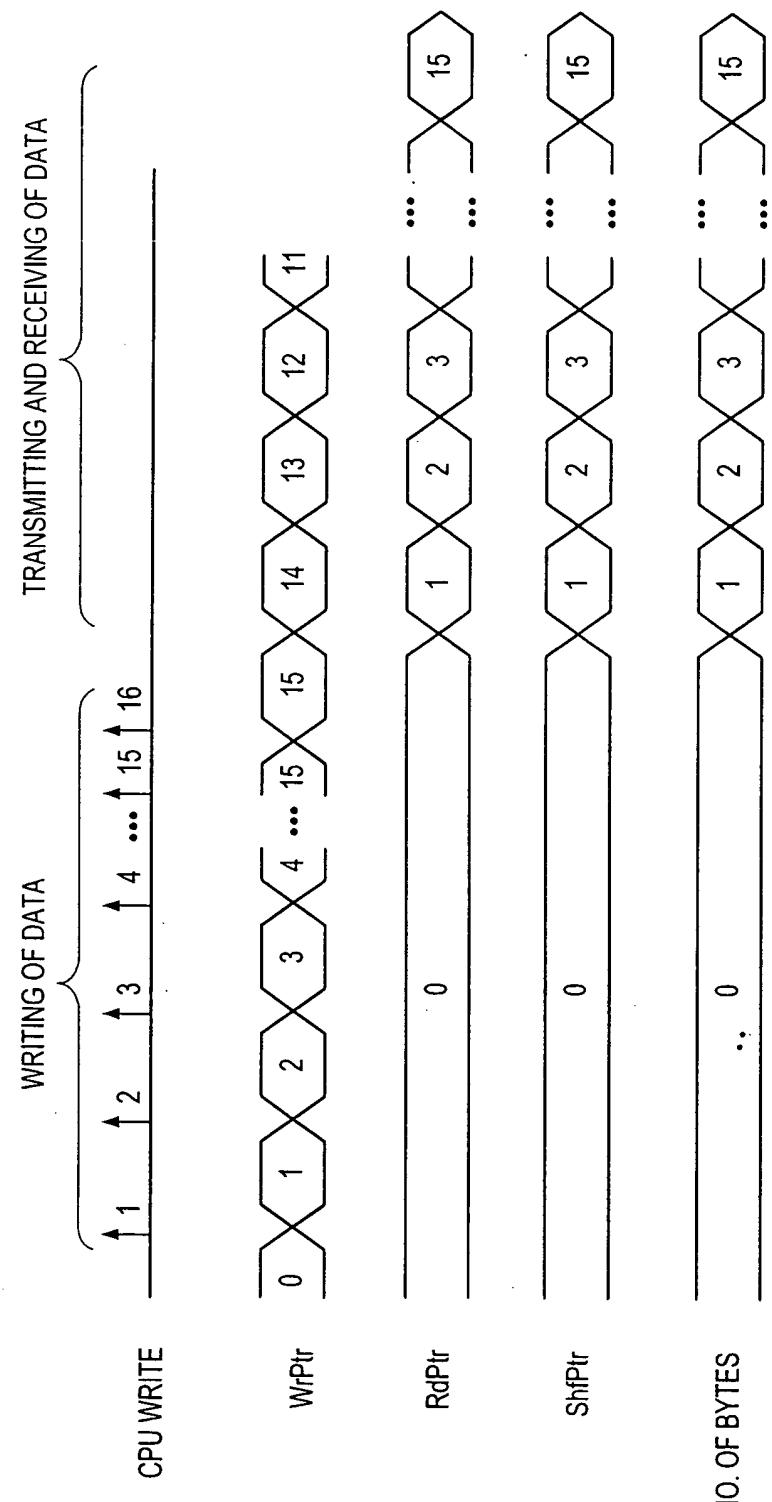


FIG.6